ABSTRACT

A coefficient computing section (121), for computing a characteristic reverse to an input/output characteristic of the power amplifier (113), is configured by a fixed coefficient storing section (104) and an error coefficient computing section (105). The fixed coefficient storing section (104) is previously stored with the characteristic reverse to a pre-measured input/output characteristic. The error coefficient computing section (105) computes an error coefficient between a characteristic stored in the fixed coefficient storing section (104) and a current characteristic of the power amplifier (113). When the determining section (123) determines that the adjacent-channel leak current power ratio is greater than a predetermined value, an operation halt is instructed to the power amplifier (113). With this structure, rapid convergence is possible in adapting operation for follow-up the characteristic variation of the power amplifier (113). Furthermore, interference to the adjacent channel due to distortion abnormality in the power amplifier (113) can be prevented.